

References

- [1] P. Augerat, C. Martin, and B. de Oliveira Stein. Scalable monitoring tools for grids and clusters. In *10th Euromicro Workshop on Parallel, Distributed and Network-Based Processing*. IEEE Computer Society Press, January 2002.
- [2] R. B. Avila. *Un modèle de distribution de serveur de fichiers pour grappes*. PhD thesis, Insitut National Polytechnique de Grenoble, June 2005.
- [3] Rafael Bohrer Ávila, Philippe O. Navaux, Pierre Lombard, Adrien Lebre, and Yves Denneulin. Performance evaluation of a prototype nfs server. In *Proceedings of the 16th symposium on computer architecture and High-Performance Computing*, pages 100–105, 2004.
- [4] L. Baldo, L. Brenner, L. G. Fernandes, P. Fernandes, and A. Sales. Performance Models for Master/Slave Parallel Programs. *Electronic Notes In Theoretical Computer Science*, 128(4):101–121, April 2005.
- [5] M. E. Barreto, Ph. O. A. Navaux, and M. P. Rivière. Deck: a new model for a distributed executive kernel integrating communication and multithreading for support of distributed object oriented application with fault tolerance support. In *Proceedings of II CACIC*, Nelquen, 1998.
- [6] O. Beaumont, E.M. Daoudi, N. Maillard, P. Manneback, and J.-L. Roch. Tradeoff to minimize extra-computations and stopping criterion tests for parallel iterative schemes. In *3rd International Workshop on Parallel Matrix Algorithms and Applications (PMAA04)*, CIRM, Marseille, France, 18–22 october 2004.
- [7] O. Beaumont, E.M. Daoudi, N. Maillard, P. Manneback, and J.-L. Roch. Tradeoff to minimize extra-computations and stopping criterion tests for parallel iterative schemes. In *PMAA'04*, 2004.
- [8] A. Ben-Abdallah, A. S. Charão, I. Charpentier, and B. Plateau. Ahpik: A Parallel Multi-threaded Framework Using Adaptivity and Domain Decomposition Methods for Solving PDE Problems. In *Proceedings of the 13th International Conference on Domain Decomposition Methods, October 2000*. CNME UPS, Barcelone, October 2001.
- [9] A. Benoit, L. Brenner, P. Fernandes, and B. Plateau. Agregation of stochastic automata with replicas. In *International Conference on the Numerical Solution of Markov Chains*, Illinois, September 2003.
- [10] A. Benoit, L. Brenner, P. Fernandes, and B. Plateau. Aggregation of Stochastic Automata Networks with replicas. *Linear Algebra and its Applications*, 386:111–136, July 2004.
- [11] A. Benoit, L. Brenner, P. Fernandes, and B. Plateau. Agregation of stochastic automata with replicas. *Linear Algebra and its Applications*, pages 111–136, july 2004.
- [12] A. Benoit, L. Brenner, P. Fernandes, B. Plateau, and W. Stewart. The peps software tool. In *13th International Conference on Modelling Techniques and Tools for Computer Performance Evaluation*, Illinois, September 2003.
- [13] A. Benoit, P. Fernandes, B. Plateau, and J. W. Stewart. On the benefits of using functional transitions and kronecker algebra. *Performance Evaluation*, 34, 2003.

- [14] A. Benoit, P. Fernandes, B. Plateau, and W. Stewart. On the benefits of using fonctionnal transitions and kronecker algebra. *Performance Evaluation*, April 2004. <http://authors.elsevier.com/sd/article/S0166531604000550>.
- [15] Francieli Zanon Boito, Rodrigo Virote Kassick, Philippe Navaux, and Yves Denneulin. A survey on applications' i/o characterization. In *proceeding os the IX Workshop de Processamento Paralelo e Distribuído*, Porto Alegre, 2011.
- [16] Francieli Zanon Boito, Rodrigo Virote Kassick, Laércio L. Pilla, Norton Barbieri, Cláudio Schepke, Philippe Navaux, Nicolas Maillard, Yves Denneulin, Carla Osthoff, Pablo Grunmann, Pedro Dias, and Jairo Panetta. I/o performance of a large atmospheric model using pvfs. In *Renpar 20*, 2011.
- [17] L. Brenner, P. Fernandes, J.M. Fourneau, and B. Plateau. Modelling grid5000 point availability with san. In *Third International Workshop on Practical Applications of Stochastic Modelling (PASM'08)*, pages 149–162, Palma de Mallorca, Spain, 2008.
- [18] L. Brenner, P. Fernandes, B. Plateau, and I. Sbeity. Peps2007 - stochastic automata networks software tool. In *Fourth International Conference on the Quantitative Evaluation of Systems (QEST 2007)*, pages 163–164. IEEE Computer Society, 2007.
- [19] L. Brenner, P. Fernandes, and A. Sales. The Need for and the Advantages of Generalized Tensor Algebra for Kronecker Structured Representations. *International Journal of Simulation: Systems, Science & Technology*, 6(3-4):52–60, February 2005.
- [20] L. Brenner, P. Fernandes, A. Sales, and T. Webber. A Framework to Decompose GSPN models. In *26th Internaciona Conference on Application and Theory of Petri Nets and Other Models of Concurrency (ATPN'05)*, pages 128–147, Miami, USA, June 2005. Lecture Notes in Computer Science - 3536.
- [21] Leonardo Brenner. *Réseaux d'Automates Stochastiques : Analyse transitoire en temps continu et Algèbre tensorielle pour une sémantique en temps discret*. PhD thesis, Institut National Polytechnique de Grenoble, September 2009.
- [22] Leonardo Brenner, Paulo Fernandes, Jean-Michel Fourneau, and Brigitte Plateau. Modelling Grid5000 point availability with SAN. *Electronic Notes In Theoretical Computer Science*, 232:165–178, March 2009.
- [23] Leonardo Brenner, Paulo Fernandes, Brigitte Plateau, and Ihab Sbeity. Peps2007 - stochastic automata networks software tool. In *4th International Conference on the Quantitative Evaluation of SysTems (QEST) 2007*, Edimbourg, UK, September 2007.
- [24] J. Briat and A. Carissimi. Intégration de *threads* et communications: une étude de cas. In *11^{ième} Rencontres francophones du parallélisme, des architectures et des systèmes*, Rennes, France, June 1999.
- [25] J. Briat, I. Ginzburg, and M. Pasin. ATHAPASCAN-0B : un noyau exécutif parallèle. *Lettre du Calculateur Parallle*, 10(3):273–293, 1998.
- [26] A. Carissimi and M. Pasin. Athapascan: An experience on mixing mpi communications and threads. In Vassil Alexandrov and Jack Dongarra, editors, *Proceedings of 5th European PVM/MPI Users' Group Meeting*, LNCS 1497, pages 137–144, Liverpool, UK, sep 1998. Springer Verlag.

- [27] Alexandre Carissimi. *Le noyau exécutif Athapascan-0 et l'exploitation de la multiprogrammation légère sur les grappes de stations multiprocesseurs*. Thèse de doctorat en informatique, Institut National Polytechnique de Grenoble, France, November 1999.
- [28] M. Castro, L. Goes, C. Pousa, M. Cole, M. Cintra, and J-F. Méhaut. A Machine Learning-Based Approach for Thread Mapping on Transactional Memory Applications. In *18th Annual International Conference on High Performance Computing (HiPC)*, Anchorage, USA, 2011.
- [29] Márcio Castro, Luiz Gustavo Fernandes, Christiane Pousa, Jean-François Méhaut, and Marilton S. de Aguiar. NUMA-ICTM: A Parallel Version of ICTM Exploiting Memory Placement Strategies for NUMA Machines. In *PDSEC '09: Proceedings of the 23rd IEEE International Parallel and Distributed Processing Symposium - IPDPS*, Rome, Italy, 2009. IEEE Computer Society.
- [30] Márcio Castro, Kiril Georgiev, Vania Marangonzova-Martin, Jean-François Méhaut, Luiz Gustavo Fernandes, and Miguel Santana. Analysis and Tracing of Applications Based on Software Transactional Memory on Multicore Architectures. In *Euromicro International Conference on Parallel, Distributed and Network-Based Computing (PDP)*, pages 199–206, Ayia Napa, Cyprus, 2011. IEEE Computer Society.
- [31] Gerson G. H. Cavalheiro, Yves Denneulin, and Jean-Louis Roch. A general modular specification for distributed schedulers. In *EuroPar'98*, Southampton, England., September 1998.
- [32] Gerson G. H. Cavalheiro, François Galilée, and Jean-Louis Roch. Athapascan-1: Parallel Programming with Asynchronous Tasks. In *Proceedings of the Yale Multithreaded Programming Workshop*, Yale, USA, June 1998.
- [33] Gerson G. H. Cavalheiro and Jean-Louis Roch. Un schéma modulaire pour l'écriture des ordonnanceurs. In *RenPar'98*, Strasbourg, France, June 1998.
- [34] Gerson-Geraldo-Homrich Cavalheiro. *Athapascan 1 : Interface générique pour l'ordonnancement dans un environnement d'exécution parallèle*. Thèse de doctorat en informatique, Institut National Polytechnique de Grenoble, France, November 1999.
- [35] Gerson-Geraldo-Homrich Cavalheiro, Matthias Doreille, François Galilée, Thierry Gautier, and Jean-Louis Roch. Scheduling parallel programs on non-uniform memory architecture s. In *HPCA Conference – Workshop on “Parallel Computing for Irregular Applications WPCIA1”*, Orlando, USA, January 1999.
- [36] Márcia C. Cera, Yiannis Georgiou, Olivier Richard, Nicolas Maillard, and Philippe Olivier Alexandre Navaux. Supporting malleability in parallel architectures with dynamic cpusetmapping and dynamic mpi. In *ICDCN*, pages 242–257, 2010.
- [37] Marcia Cristina Cera. *Providing Adaptability to MPI Applications on Current Parallel Architectures*. PhD thesis, UFRGS, 2011.
- [38] Rafael Chanin, Mônica Corrêa, Paulo Fernandes, Afonso Sales, Roque Scheer, and Avelino F. Zorzo. Analytical modeling for operating system schedulers on numa systems. *Electr. Notes Theor. Comput. Sci.*, 151(3):131–149, 2006.

- [39] A. S. Charão, I. Charpentier, and B. Plateau. A framework for parallel multithreaded implementation of domain decomposition methods. In *Proceedings of Parallel Computing'99*, Delft, The Netherlands, August 1999.
- [40] A. S. Charão, I. Charpentier, and B. Plateau. A framework for parallel multithreaded implementation of domain decomposition methods. In E. H. D'Hollander, G. R. Joubert, F. J. Peters, and H. J. Sips, editors, *Parallel Computing: Fundamentals and Applications*, pages 95–102. Imperial College Press, 1999.
- [41] A. S. Charão, I. Charpentier, and B. Plateau. Generic parallel programming of domain decomposition methods on PC clusters. In *Proceedings of the 13th International Conference on Domain Decomposition Methods*. Cocoyoc, Morelos, Mexico, janvier 2002.
- [42] Andréa Schwertner Charão. *Multiprogrammation parallèle générique des méthodes de décomposition de domaine*. Thèse de doctorat, Institut National Polytechnique de Grenoble, September 2001.
- [43] J. Chassin de Kergommeaux and B. de Oliveira Stein. Pajé: an extensible and interactive and scalable environment for visualizing parallel executions. Technical report, INRIA, 2000.
- [44] J. Chassin de Kergommeaux and B. de Oliveira Stein. Pajé: an extensible environment for visualizing multi-threaded programs executions. In A. Bode, W. Ludwig, T. Karl, and R. Wismüller, editors, *Euro-Par 2000 Parallel Processing, Proc. 6th International Euro-Par Conference*, volume 1900 of *LNCS*, pages 133–140. Springer, 2000.
- [45] J. Chassin de Kergommeaux, B. de Oliveira Stein, and P.-E. Bernard. Pajé, an interactive visualization tool for tuning multi-threaded parallel applications. *Parallel Computing*, 26(10):1253–1274, August 2000.
- [46] Daniel Cordeiro, Denis Trystram, and Frédéric Wagner. Analysis of multi-organization scheduling algorithms. In *International Conference on Parallel Computing (Euro-Par)*, 2010.
- [47] E. Cruz, C. Pousa, M. Alves, A. Carissimi, P. Navaux, and J-F. Méhaut. Using Memory Access Traces to Map Threads and Data on Hierarchical Multi-core Platforms. In *Workshop on Advances on Parallel and Distributed Processing Symposium (APDCM 2011)*, Urbana Champaign, USA, 2011.
- [48] E. Cruz, C. Pousa, M. Alves, A. Carissimi, P. Navaux, and J-F. Méhaut. Using Memory Access Traces to Map Threads and Data on Hierarchical Multi-core Platforms. *International Journal on Networking and Computing*, To be published. Extended version of [47].
- [49] Ricardo Czekster, Paulo Fernandes, Jean-Marc Vincent, and Thais Webber. Split: a flexible and efficient algorithm to vector-descriptor product. In *SMCtools*, October 2007.
- [50] J. Chassin de Kergommeaux and B. de Oliveira Stein. Flexible performance visualization of parallel and distributed applications. *Future Generation Computer Systems*, 19:735–747, 2003.
- [51] B. de Oliveira Stein and J. Chassin de Kergommeaux. Interactive visualisation environment of multi-threaded parallel programs. In *Parallel Computing: Fundamentals, Applications and New Directions*, pages 311–318. Elsevier, 1998.

- [52] Benhur de Oliveira Stein. *Visualisation interactive et extensible de programmes parallèles base de processus légers*. Thèse de doctorat en informatique, Institut National Polytechnique de Grenoble, France, October 1999.
- [53] C. de Rose, F. Blancoa, N. Maillard, Katia B. Saikoski, R. Novaes, O. Richard, and B. Richard. The virtual cluster: A dynamic environment for exploitation of idle network resources. In *SBAC-PAD (14th Symposium on Computer Architecture and High Performance Computing)*, pages 141–150. IEEE Computer Society, 2002.
- [54] Afonso Corra de Sales. *Réseaux d’Automates Stochastiques : Génération de l’espace d’états atteignables et Multiplication vecteur-descripteur pour une sémantique en temps discret*. PhD thesis, Institut National Polytechnique de Grenoble, September 2009.
- [55] Bruno Donassolo, Henri Casanova, Arnaud Legrand, and Pedro Velho. Fast and scalable simulation of volunteer computing systems using simgrid. In *Workshop on Large-Scale System and Application Performance (LSAP)*, 2010.
- [56] Bruno Donassolo, Arnaud Legrand, and Claudio Geyer. Non-Cooperative Scheduling Considered Harmful in Collaborative Volunteer Computing Environments. In *Proceedings of the 11th IEEE International Symposium on Cluster Computing and the Grid (CC-Grid’11)*. IEEE Computer Society Press, may 2011.
- [57] Fabrice Dupros, Christiane Pousa, Alexandre Carissimi, and Jean-François Méhaut. Parallel Simulations of Seismic Wave Propagation on NUMA Architectures. In *ParCo’09: International Conference on Parallel Computing (to appear)*, Lyon, France, 2009.
- [58] Jean-François Méhaut Fabrice Dupros, Alexandre Carissimi. Sauvegarde et reprise d’applications parallèles mpi dans le cadre d’un intranet. In *Rencontres Francophones du Parallélisme*, 2006.
- [59] Luis Gustavo Fernandes, Nicolas Maillard, and Yves Denneulin. Parallelizing a dense matching region growing algorithm for an image interpolation application. In *Proceedings of the International Conference on Parallel and Distributed Processing Techniques and Applications (PDPTA’2001)*, Las Vegas, June 2001.
- [60] P. Fernandes. *Méthodes numériques pour la solution de systèmes markoviens à grand espace d’états*. PhD thesis, Insitut National Polytechnique de Grenoble, feb 1998.
- [61] P. Fernandes and B. Plateau. Triangular solution of linear systems in tensor product format. In *Workshop on Mathematical Modelling and Analysis, MAMA 2000*, Santa Clara, California, USA, june 2000.
- [62] P. Fernandes, B. Plateau, and W.J. Stewart. Efficient descriptor-vector multiplications in stochastic automata networks. *JACM*, (3):381–414, 1998.
- [63] P. Fernandes, B. Plateau, and W.J. Stewart. Optimizing tensor product computations in stochastic automata networks. *RAIRO, Operations Research*, (3):325–351, 1998.
- [64] Paulo Fernandes, Jean-Marc Vincent, and Thais Webber. Split: a flexible and efficient algorithm to vector-descriptor product. In *ASMTA 2008*, Nicosia, Cyprus, June 2008.
- [65] François Galilée, Jean-Louis Roch, Gerson G. H. Cavalheiro, and Mahtias Doreille. Athapascan-1: On-line Building Data Flow Graph in a Parallel Language. In IEEE, editor, *International Conference on Parallel Architectures and Compilation Techniques, PACT’98*, pages 88–95, Paris, France, October 1998.

- [66] C. Guilloud, P. Augerat, J. Chassin de Kergommeaux, and B. Stein. Outil visuel d'administration système pour grappe de processeurs. In *RenPar'13*, pages 163–168, April 2001.
- [67] E. Hermann, R. Kassick, R. Avila, C. Barrios, M. Riveill, Y. Denneulin, and P. Navaux. Performance evaluation of meta-data transfer and storage in clusters. In *American Conference of High Performance Computing*, Santa Marta, Colombia, August 2007.
- [68] Everton Hermann. *Interactive Physical Simulation on Multi-Core and Multi-GPU Architectures*. PhD thesis, Grenoble INP, June 2010.
- [69] Everton Hermann, Rafael Bohrer Ávila, Philippe O. Navaux, and Yves Denneulin. Metaserver locality and scalability in a distributed nfs. In *Proceedings of the 7th international meeting on high performance (Vecpar'06) computing for computational science*, 2006.
- [70] Everton Hermann, François Faure, and Bruno Raffin. Ray-traced Collision Detection for Deformable Bodies. In *3rd International Conference on Computer Graphics Theory and Applications (GRAPP)*, pages 293–299, Madeira, Portugal, January 2008.
- [71] Everton Hermann, Bruno Raffin, and François Faure. Interactive Physical Simulation on Multicore Architectures. In *Eurographics 2009 Symposium on Parallel Graphics and Visualization (EGPGV'09)*, pages 1–8, Munich, Germany, March 2009.
- [72] Everton Hermann, Bruno Raffin, Franoise Faure, Thierry Gautier, and Jérémie Allard. Multi-GPU and Multi-CPU Parallelization for Interactive Physics Simulations. In *Europar 2010*, September 2010.
- [73] G. Huard, L. Schnorr, and P. A. Navaud. 3d approach to the visualization of parallel applications and grid monitoring information. In *9th IEEE/ACM International Conference on Grid Computing (Grid 2008)*, Tsukuba, Japan, 2008.
- [74] C. Guilloud J. Chassin de Kergommeaux and B. de Oliveira Stein. Flexible performance debugging of parallel and distributed applications. In *Euro-Par Conference*, number 2790 in LNCS, August 2003.
- [75] Rodrigo Kassick, Caciano Machado, Everton Hermann, Rafael Bohrer Ávila, Philippe O. Navaux, and Yves Denneulin. Evaluating the performance of the dnfsp file system. In *Proceedings of CCGrid 2005*, Cardiff, Pays de Galles, 2005.
- [76] Rodrigo Kassick, Francieli Zanon, Nicolas Maillard, Philippe Navaux, Roberto Souto, Haroldo Velho, Bruno Bzeznik, Olivier Richard, Guillermo Berri, and Obidio Rubio-Mercedes. Gbrams-amsud: Latin-american grid for climatology. In *CLCAR*, 2010.
- [77] Rodrigo Virote Kassick, Carla Osthoff, Philippe Navaux, Francieli Zanon Boito, Cláudio Schepke, Nicolas Maillard, Matthias Diener, and Yves Denneulin. Trace-based visualization as a tool to understand applications i/o performance. In *proceeding of the SBAC-PAD 2011 - WAMCA 2011 workshop*, 2011.
- [78] P. Kayser Vargas, D. N. Ferrari, C. F. R. Geyer, J. L. V. Barbosa, and J. Chassin de Kergommeaux. Distributed or scheduling with granularity informations. In *Proceedings of the 12th Symposium on Computer Architecture and High Performance Computing, SBAC-PAD'2000*, 2000.

- [79] Philippe Olivier Alexandre Navaux Lucas Mello Schnorr, Guillaume Huard. Towards visualization scalability through time intervals and hierarchical organization of monitoring data. In *CCGRID '09: Proceedings of the 2009 9th IEEE/ACM International Symposium on Cluster Computing and the Grid*, pages 428–435, 2009.
- [80] Philippe Olivier Alexandre Navaux Lucas Mello Schnorr, Guillaume Huard. Visual mapping of program components to resources representation: a 3d analysis of grid parallel applications. In *SBAC-PAD '09: Proceedings of the 21st International Symposium on Computer Architecture and High Performance Computing*, 2009.
- [81] É. Morel, J. Briat, J. Chassin de Kergommeaux, and C. Geyer. Side-effects in PloSys OR-parallel Prolog on distributed Memory Machines. In *Parallelism and Implementation of Logic and Constraint Logic Programming*, chapter 9. NOVA Science Publishers, Inc, New York, USA, 1999.
- [82] P. Oliveira, H. C. Freitas, C. P. Ribeiro, M. Castro, V. Marangonzova-Martin, and J.-F. Méhaut. Performance Evaluation of WiNoCs for Parallel Workloads Based on Collective Communications. In *IADIS International Conference on Applied Computing (AC)*, Rio de Janeiro, Brazil, 2011. IADIS Press.
- [83] F.-G. Ottogalli, C. Labbé, V. Olive, B. Oliveira Stein, J. Chassin de Kergommeaux, and J.-M. Vincent. Visualisation of distributed applications for performance debugging. In V. Alexandrov, J. Dongarra, B. Juliano, R. Renner, and C.J. Kenneth Tan, editors, *ICCS'01: International Conference in Computational Science*, LNCS 2074, pages 831–840, Berlin, Heidelberg, 2001. Springer.
- [84] Mareclo Pasin. *Mouvement efficace de données pour la programmation parallèle irrégulière*. Thèse de doctorat en informatique, Institut National Polytechnique de Grenoble, France, November 1999.
- [85] M. Pillon. *DRAC: Un système de contrôle d'exécution pour multiprocesseur mémoire partagée*. PhD thesis, Insitutit National Polytechnique de Grenoble, Novembre 2004.
- [86] Maurício Pillon. Utilisations des compteurs matériels dans le multiprocesseurs: estimation de l'accélération et contrôle-commande d'exécution. In *14èmes Rencontres Francophones du Parallélisme*, pages 193–200, avril 2002.
- [87] Mauricio Pillon and Olivier Richard. Escalonamento adaptativo ao uso da hierarquia de memoria para maquinas multiprocessadas. In *WSCAD - Quinto Workshop em Sistemas Computacionais de Alto Desempenho, 2004, Foz do Iguau - Brazil*. SBC - Sociedade Brasileira da Computao, 2004.
- [88] Mauricio Pillon, Olivier Richard, and Georges Da-Costa. Drac: Adaptive control system with hardware performance counters. In *Euro-Par 2004, Parallel Processing, International Euro-Par Conference Paderborn, Italy, August 31 - Sept. 3, 2004, Proceedings*, Lecture Notes in Computer Science. Springer, 2004.
- [89] Brigitte Plateau and A. Sales. Reachable state space generation for structured models which use functional transitions. In *Proceedings of the 6th International Conference on the Quantitative Evaluation of Systems (QEST'09)*, Budapest, Hungary, September 2009. IEEE Computer Society.

- [90] C. R. Pousa, M. Castro, J-F. Méhaut, and A. Carissimi. Improving memory affinity of geophysics applications on numa platforms using minas. In *9th International Meeting High Performance Computing for Computational Science (VecPar)*, 2010.
- [91] C. R. Pousa, N. Maillard, I. Stangherlini, and J-F. Méhaut. Compiling openmp applications to enhance memory affinity on hierarchical multi-core machines (poster). In *23rd International Workshop on Languages and Compilers for Parallel Computing*, 2010.
- [92] C. R. Pousa, J-F. Méhaut, and A. Carissimi. Memory affinity management for numerical scientific applications over multi-core multiprocessors with hierarchical memory. In *PhD Forum of 24th IEEE International Parallel and Distributed Processing Symposium (IPDPS)*, 2010.
- [93] Christiane Pousa, Márcio Castro, Luiz Gustavo Fernandes, Alexandre Carissimi, and Jean-François Méhaut. Memory Affinity for Hierarchical Shared Memory Multiprocessors. In *21st International Symposium on Computer Architecture and High Performance Computing - SBAC-PAD*, São Paulo, Brazil, 2009. IEEE.
- [94] Christiane Pousa, Márcio Castro, Luiz Gustavo Fernandes, Fabrice Dupros, Alexandre Carissimi, and Jean-François Méhaut. High Performance Applications on Hierarchical Shared Memory Multiprocessors. In *Colloque d'Informatique: Brésil / INRIA, Coopérations, Avancés et Défis*, São Bento, Brazil, 2009. SBC.
- [95] C. Ribeiro, J.-F. Mehaut V. Marangozova-Martin, F. Dupros, and A. Carissimi. Explorando afinidade de memria em arquiteturas numa. In *Workshop em Sistemas Computacionais de Alto Desempenho*, Campo Granden Brazil, 2008.
- [96] C. P. Ribeiro, M. Castro, J.-F. Méhaut, V. Marangonzova-Martin, H. C. Freitas, and C. A. P. S. Martins. Investigating the Impact of CPU and Memory Affinity on Multi-core Platforms: A Case Study of Numerical Scientific Multithreaded Applications. In *IADIS International Conference on Applied Computing (AC)*, Rio de Janeiro, Brazil, 2011. IADIS Press.
- [97] Christiane Pousa Ribeiro. *Contributions on Memory Affinity Management for Hierarchical Shared Memory Multi-core Platforms*. PhD thesis, Grenoble Université, July 2011.
- [98] Bruno Richard, Nicolas Maillard, Csra De Rose, and Reynaldo Novaes. The i-cluster cloud: Distributed management of idle resources for intense computing. *Parallel Computing*, 2005.
- [99] I. Sbeity, L. Brenner, B. Plateau, and W.J. Stewart. Phase-type distributions in stochastic automata networkss. *European Journal of Operational Research*, 2007.
- [100] Lucas Mello Schnorr. *Some Visualization Models applied to the Analysis of Parallel Applications*. PhD thesis, Institut National Polytechnique de Grenoble, October 2009.
- [101] Lucas Mello Schnorr, Guillaume Huard, and Philippe O.A. Navaux. Triva: Interactive 3d visualization for performance analysis of parallel applications. *Future Generation Computer Systems*, 26(3):348 – 358, 2010.
- [102] D. Stringhini, P. Navaux, and J. Chassin de Kergommeaux. A selection mechanism to group processes in a parallel debugger. In *Proc. of the International Conference on Parallel and Distributed Processing Techniques and Applications, PDPTA '2000*, pages 2575–2581, Las Vegas, USA, June 2000. CSREA Press.

- [103] D. Traoure, J.-L. ROCH, N. Maillard, T. Gautier, and J. Bernard. Deque-free work-optimal parallel stl algorithm. In *Europar 2008*, pages 887–897, Spain, 2008.
- [104] Pedro Velho. *Accurate and Fast Simulations of Large-Scale Distributed Computing Systems*. PhD thesis, Grenoble Université, July 2011.
- [105] J. VERDUZCO, N. GARCIA, J. PECERO, P. DOMINGUEZ, M. PILLON, N. CAPIT, O. RICHARD, F. BENAVIDEZ, P. FLETES, and M. MARTINEZ. Grid-itc: Una intragrid de bajo costo para las necesidades de calculo intensivo del instituto tecnologico de colima. In *VI Jornadas Internacionales de las Ciencias Computacionales, 2004, Colima - Mexique.*, 2004.
- [106] Thais Webber. *Reducing the Impact of State Space Explosion in Stochastic Automata Networks*. PhD thesis, Pontificia Universidade do Rio Grande do Sul (PUCRS), March 2009.